

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Keith R. Jones, et al.)
 Patent No. : 6,934,655 B2)
 Issued : August 23, 2005)
 Appl. No. : 09/810,932)
 Filed : March 16, 2001)
 For : METHOD AND APPARATUS FOR)
 TRANSMISSION LINE ANALYSIS)



I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Attn: Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

September 20, 2005
 (Date)

Chad W. Miller, Reg. No. 44,943

REQUEST FOR CERTIFICATE OF CORRECTION

Attn: Certificate of Corrections Branch
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Certificate
 SEP 28 2005
 of Correction

Dear Sir:

Enclosed for filing is a Certificate of Correction with respect to the above-referenced patent. The errors in the patent, which are cited in the Certificate of Correction, were incurred through a mistake on the part of the Patent Office, and therefore a fee is not required in accordance with 37 C.F.R. Section 1.322. Applicants respectfully submit that these mistakes do not involve changes that would constitute new matter or require reexamination of the patent.

Column 11, between lines 55 and 56, please insert the following paragraph:

In one example method of operation, a continuous sequence of logic value 1's is provided to the input 500. The state of each register may be selectively loaded with a logical one or logical zero based on the desired sequence to be generated. When provided with a string of logics one values, the generator outputs a unique string, or sequence, of 1's or 0's based on the values of the registers 510. In one embodiment, the values loaded into the registers are selected to form a primitive polynomial known to generate a maximal length sequence (M-sequence). The sequence will repeat through the $2^N - 1$ non-zero states.

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Patent No. : 6,934,655
Issued : August 23, 2005

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Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 6,934,655 B2

DATED : August 23, 2005

INVENTOR(S) : Keith R. Jones, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11, between lines 55 and 56, please insert the following paragraph:

In one example method of operation, a continuous sequence of logic value 1's is provided to the input 500. The state of each register may be selectively loaded with a logical one or logical zero based on the desired sequence to be generated. When provided with a string of logics one values, the generator outputs a unique string, or sequence, of 1's or 0's based on the values of the registers 510. In one embodiment, the values loaded into the registers are selected to form a primitive polynomial known to generate a maximal length sequence (M-sequence). The sequence will repeat through the $2^N - 1$ non-zero states.

MAILING ADDRESS OF SENDER (Please do not use customer number)

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PATENT NO. 6,934,655

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